

Subject index for papers in Volume 50

Each index entry below is accompanied by an author's name and a page number; the author index contains the title of the paper and the names of coauthors, if any.

Subject	Author	Page			
<i>Algorithms</i>			The Pathway Editor: A tool for managing complex biological networks	Sorokin	561
An approximation to the greedy algorithm for differential compression	Agarwal	149	<i>Carrier transport</i>		
Braids and fibers: Language constructs with architectural support for adaptive responses to memory latencies	Bacon	209	Germanium channel MOSFETs: Opportunities and challenges	Shang	377
Machine learning methods for transcription data integration	Holloway	631	<i>Carrier transport in small structures</i>		
Self-adapting numerical software (SANS) effort	Dongarra	223	Continuous MOSFET performance increase with device scaling: The role of strain and channel material innovations	Antoniadis	363
<i>Arrays</i>			<i>Circuit and device technology</i>		
Design considerations for MRAM	Maffitt	25	High-performance CMOS variability in the 65-nm regime and beyond	Bernstein	433
<i>Biology and biomedical studies</i>			Limited switch dynamic logic circuits for high-speed low-power circuit design	Belluomini	277
A spatially detailed myofilament model as a basis for large-scale biological simulations	Hussan	583	Optimizing CMOS technology for maximum performance	Frank	419
An assessment of the role of computing in systems biology	Burbeck	529	Silicon CMOS devices beyond scaling	Haensch	339
Computational multiscale modeling in the IUPS Physiome Project: Modeling cardiac electromechanics	Nickerson	617	Three-dimensional integrated circuits	Topol	491
Graph data management for molecular and cell biology	Eckman	545	Ultralow-voltage, minimum-energy CMOS	Hanson	469
Machine learning methods for transcription data integration	Holloway	631	<i>CMOS</i>		
Model-based design approaches in drug discovery: A parallel to traditional engineering approaches	Schoeberl	645	Advanced high- κ dielectric stacks with polySi and metal gates: Recent progress and current challenges	Gusev	387
Multiscale biosystems integration: Coupling intracellular network analysis with tissue-patterning simulations	Peirce	601	Emerging nanoscale silicon devices taking advantage of nanostructure physics	Hiramoto	411
The Pathway Editor: A tool for managing complex biological networks	Sorokin	561	High-performance CMOS variability in the 65-nm regime and beyond	Bernstein	433
Visualization of complementary systems biology data with parallel heatmaps	Podowski	575	Limited switch dynamic logic circuits for high-speed low-power circuit design	Belluomini	277
<i>Biotechnology</i>			Optimizing CMOS technology for maximum performance	Frank	419
A spatially detailed myofilament model as a basis for large-scale biological simulations	Hussan	583	Product-representative "at speed" test structures for CMOS characterization	Ketchen	451
An assessment of the role of computing in systems biology	Burbeck	529	Silicon CMOS devices beyond scaling	Haensch	339
Computational multiscale modeling in the IUPS Physiome Project: Modeling cardiac electromechanics	Nickerson	617	Ultralow-voltage, minimum-energy CMOS	Hanson	469
Machine learning methods for transcription data integration	Holloway	631	<i>Compilers and interpreters</i>		
Multiscale biosystems integration: Coupling intracellular network analysis with tissue-patterning simulations	Peirce	601	Systems research challenges: A scale-out perspective	Agerwala	173
			<i>Computation</i>		
			An approximation to the greedy algorithm for differential compression	Agarwal	149
			<i>Computational methods</i>		
			Machine learning methods for transcription data integration	Holloway	631
			Self-adapting numerical software (SANS) effort	Dongarra	223

<i>Computer applications</i>				<i>Dielectrics</i>			
Building web services for scientific grid applications	Kandaswamy	249		Advanced high- κ dielectric stacks with polySi and metal gates: Recent progress and current challenges	Gusev	387	
<i>Computer architecture</i>				<i>Drug design</i>			
Application of full-system simulation in exploratory system design and development	Peterson	321		Model-based design approaches in drug discovery: A parallel to traditional engineering approaches	Schoeberl	645	
Decomposing the load-store queue by function for power reduction and scalability	Baugh	287		<i>Electroluminescence</i>			
IBM Intelligent Bricks project—Petabytes and beyond	Wilcke	181		Highly efficient room-temperature tunnel spin injector using CoFe/MgO(001)	Jiang	111	
Reliability of modular mesh-connected intelligent storage brick systems	Fleiner	199		<i>Error detection and correction</i>			
Systems research challenges: A scale-out perspective	Agerwala	173		HeapMon: A helper-thread approach to programmable, automatic, and low-overhead memory bug detection	Shetty	261	
<i>Computer organization and design</i>				<i>Fault tolerance</i>			
Application of full-system simulation in exploratory system design and development	Peterson	321		IBM Intelligent Bricks project—Petabytes and beyond	Wilcke	181	
Decomposing the load-store queue by function for power reduction and scalability	Baugh	287		<i>Films, magnetic</i>			
IBM Intelligent Bricks project—Petabytes and beyond	Wilcke	181		Two-level BEOL processing for rapid iteration in MRAM development	Gaidis	41	
Reliability of modular mesh-connected intelligent storage brick systems	Fleiner	199		<i>Films, semiconductor</i>			
Systems research challenges: A scale-out perspective	Agerwala	173		Continuous MOSFET performance increase with device scaling: The role of strain and channel material innovations	Antoniadis	363	
Victim management in a cache hierarchy	Franaszek	507		<i>Geonomic and proteomic analysis</i>			
<i>Computer system availability</i>				Model-based design approaches in drug discovery: A parallel to traditional engineering approaches	Schoeberl	645	
IBM Intelligent Bricks project—Petabytes and beyond	Wilcke	181		Visualization of complementary systems biology data with parallel heatmaps	Podowski	575	
Reliability of modular mesh-connected intelligent storage brick systems	Fleiner	199		<i>Graph theory</i>			
<i>Computing, grid</i>				Graph data management for molecular and cell biology	Eckman	545	
Building web services for scientific grid applications	Kandaswamy	249		<i>Graphics</i>			
<i>Cooling</i>				The Pathway Editor: A tool for managing complex biological networks	Sorokin	561	
IBM Intelligent Bricks project—Petabytes and beyond	Wilcke	181		<i>Hall effect</i>			
<i>Data, structures and accessing</i>				Toward dissipationless spin transport in semiconductors	Bernevig	141	
Graph data management for molecular and cell biology	Eckman	545		<i>Insulators</i>			
Visualization of complementary systems biology data with parallel heatmaps	Podowski	575		Advanced high- κ dielectric stacks with polySi and metal gates: Recent progress and current challenges	Gusev	387	
<i>Databases, relational</i>				<i>Integrated circuit design</i>			
Graph data management for molecular and cell biology	Eckman	545		Optimizing CMOS technology for maximum performance	Frank	419	
<i>Device design</i>				Ultralow-voltage, minimum-energy CMOS	Hanson	469	
Application of full-system simulation in exploratory system design and development	Peterson	321		<i>Integrated circuits</i>			
Decomposing the load-store queue by function for power reduction and scalability	Baugh	287		Three-dimensional integrated circuits	Topol	491	
Design considerations for MRAM	Maffitt	25		<i>Interconnection technology</i>			
Development of the magnetic tunnel junction MRAM at IBM: From first junctions to a 16-Mb MRAM demonstrator chip	Gallagher	5		Two-level BEOL processing for rapid iteration in MRAM development	Gaidis	41	
Modeling wire delay, area, power, and performance in a simulation infrastructure	Carter	311		<i>Interfaces</i>			
Spintronics—A retrospective and perspective	Wolf	101		Continuous MOSFET performance increase with device scaling: The role of strain and channel material innovations	Antoniadis	363	

The Pathway Editor: A tool for managing complex biological networks	Sorokin	561	adaptive responses to memory latencies	Bacon	209
<i>Junctions</i>			HeapMon: A helper-thread approach to programmable, automatic, and low-overhead memory bug detection	Shetty	261
Rapid-turnaround characterization methods for MRAM development	Abraham	55	Victim management in a cache hierarchy	Franaszek	507
Single-domain model for toggle MRAM	Worledge	69	<i>Memory (computer) management</i>		
<i>Large-scale computing</i>			Braids and fibers: Language constructs with architectural support for adaptive responses to memory latencies	Bacon	209
Decomposing the load-store queue by function for power reduction and scalability	Baugh	287	Victim management in a cache hierarchy	Franaszek	507
Limited switch dynamic logic circuits for high-speed low-power circuit design	Belluomini	277	<i>Memory, cache</i>		
<i>Logic design and technology</i>			High-quality ISA synthesis for low-power cache designs in embedded microprocessors	Cheng	299
Decomposing the load-store queue by function for power reduction and scalability	Baugh	287	Victim management in a cache hierarchy	Franaszek	507
Limited switch dynamic logic circuits for high-speed low-power circuit design	Belluomini	277	<i>Memory, random-access</i>		
<i>Magnetic random access memory (MRAM)</i>			Development of the magnetic tunnel junction MRAM at IBM: From first junctions to a 16-Mb MRAM demonstrator chip	Gallagher	5
Design considerations for MRAM	Maffitt	25	<i>Microprocessor systems and applications</i>		
Development of the magnetic tunnel junction MRAM at IBM: From first junctions to a 16-Mb MRAM demonstrator chip	Gallagher	5	Application of full-system simulation in exploratory system design and development	Peterson	321
Rapid-turnaround characterization methods for MRAM development	Abraham	55	Decomposing the load-store queue by function for power reduction and scalability	Baugh	287
Single-domain model for toggle MRAM	Worledge	69	Modeling wire delay, area, power, and performance in a simulation infrastructure	Carter	311
Spin angular momentum transfer in current-perpendicular nanomagnetic junctions	Sun	81	<i>Models and modeling</i>		
Spintronics—A retrospective and perspective	Wolf	101	A spatially detailed myofilament model as a basis for large-scale biological simulations	Hussan	583
Two-level BEOL processing for rapid iteration in MRAM development	Gaidis	41	Application of full-system simulation in exploratory system design and development	Peterson	321
<i>Magnetics—studies and structures</i>			Computational multiscale modeling in the IUPS Physiome Project: Modeling cardiac electromechanics	Nickerson	617
Bipolar spintronics: Fundamentals and applications	Žutić	121	Model-based design approaches in drug discovery: A parallel to traditional engineering approaches	Schoeberl	645
Development of the magnetic tunnel junction MRAM at IBM: From first junctions to a 16-Mb MRAM demonstrator chip	Gallagher	5	Modeling wire delay, area, power, and performance in a simulation infrastructure	Carter	311
Highly efficient room-temperature tunnel spin injector using CoFe/MgO(001)	Jiang	111	Multiscale biosystems integration: Coupling intracellular network analysis with tissue-patterning simulations	Peirce	601
Rapid-turnaround characterization methods for MRAM development	Abraham	55	Single-domain model for toggle MRAM	Worledge	69
Single-domain model for toggle MRAM	Worledge	69	<i>Molecular structure modeling</i>		
Spin angular momentum transfer in current-perpendicular nanomagnetic junctions	Sun	81	A spatially detailed myofilament model as a basis for large-scale biological simulations	Hussan	583
<i>Magnetoresistance</i>			<i>Multimedia</i>		
Spintronics—A retrospective and perspective	Wolf	101	Three-dimensional integrated circuits	Topol	491
<i>Manufacturing</i>			<i>Nanoscale structures and devices</i>		
Product-representative “at speed” test structures for CMOS characterization	Ketchen	451	Emerging nanoscale silicon devices taking advantage of nanostructure physics	Hiramoto	411
<i>Mathematical functions and techniques</i>					
Machine learning methods for transcription data integration	Holloway	631			
<i>Memory (computer) design and technology</i>					
Braids and fibers: Language constructs with architectural support for					

Spin angular momentum transfer in current-perpendicular nanomagnetic junctions	Sun	81	Toward dissipationless spin transport in semiconductors	Bernevig	141
<i>Numerical integration</i>			<i>Silicon</i>		
Self-adapting numerical software (SANS) effort	Dongarra	223	Optimizing CMOS technology for maximum performance	Frank	419
<i>Operating systems</i>			Ultralow-voltage, minimum-energy CMOS	Hanson	469
Systems research challenges: A scale-out perspective	Agerwala	173	<i>Silicon-germanium</i>		
<i>Parallel processing</i>			Continuous MOSFET performance increase with device scaling: The role of strain and channel material innovations	Antoniadis	363
Systems research challenges: A scale-out perspective	Agerwala	173	Germanium channel MOSFETs: Opportunities and challenges	Shang	377
<i>Performance analysis</i>			<i>Silicon-on-insulator (SOI)</i>		
Optimizing CMOS technology for maximum performance	Frank	419	Germanium channel MOSFETs: Opportunities and challenges	Shang	377
Performance and environment monitoring for continuous program optimization	Caşcaval	239	<i>Simulation</i>		
Product-representative “at speed” test structures for CMOS characterization	Ketchen	451	A spatially detailed myofilament model as a basis for large-scale biological simulations	Hussan	583
<i>Physics, solid state</i>			An assessment of the role of computing in systems biology	Burbeck	529
Bipolar spintronics: Fundamentals and applications	Žutić	121	Application of full-system simulation in exploratory system design and development	Peterson	321
Emerging nanoscale silicon devices taking advantage of nanostructure physics	Hiramoto	411	Computational multiscale modeling in the IUPS Physiome Project: Modeling cardiac electromechanics	Nickerson	617
Silicon CMOS devices beyond scaling	Haensch	339	Modeling wire delay, area, power, and performance in a simulation infrastructure	Carter	311
Toward dissipationless spin transport in semiconductors	Bernevig	141	Multiscale biosystems integration: Coupling intracellular network analysis with tissue-patterning simulations	Peirce	601
<i>Power management</i>			<i>Spintronics</i>		
High-performance CMOS variability in the 65-nm regime and beyond	Bernstein	433	Bipolar spintronics: Fundamentals and applications	Žutić	121
High-quality ISA synthesis for low-power cache designs in embedded microprocessors	Cheng	299	Design considerations for MRAM	Maffitt	25
Optimizing CMOS technology for maximum performance	Frank	419	Development of the magnetic tunnel junction MRAM at IBM: From first junctions to a 16-Mb MRAM demonstrator chip	Gallagher	5
Ultralow-voltage, minimum-energy CMOS	Hanson	469	Highly efficient room-temperature tunnel spin injector using CoFe/MgO(001)	Jiang	111
<i>Process control and development</i>			Rapid-turnaround characterization methods for MRAM development	Abraham	55
Three-dimensional integrated circuits	Topol	491	Single-domain model for toggle MRAM	Worledge	69
<i>Programming systems</i>			Spin angular momentum transfer in current-perpendicular nanomagnetic junctions	Sun	81
High-quality ISA synthesis for low-power cache designs in embedded microprocessors	Cheng	299	Spintronics—A retrospective and perspective	Wolf	101
Performance and environment monitoring for continuous program optimization	Caşcaval	239	Toward dissipationless spin transport in semiconductors	Bernevig	141
<i>Semiconductor devices</i>			Two-level BEOL processing for rapid iteration in MRAM development	Gaidis	41
Silicon CMOS devices beyond scaling	Haensch	339	<i>Testing</i>		
<i>Semiconductor technology</i>			HeapMon: A helper-thread approach to programmable, automatic, and low-overhead memory bug detection	Shetty	261
Continuous MOSFET performance increase with device scaling: The role of strain and channel material innovations	Antoniadis	363	<i>Testing, chip</i>		
Optimizing CMOS technology for maximum performance	Frank	419	Rapid-turnaround characterization methods for MRAM development	Abraham	55
Ultralow-voltage, minimum-energy CMOS	Hanson	469			
<i>Semiconductors</i>					
Highly efficient room-temperature tunnel spin injector using CoFe/MgO(001)	Jiang	111			

Two-level BEOL processing for rapid iteration in MRAM development	Gaidis	41
<i>Testing, circuit</i>		
Product-representative “at speed” test structures for CMOS characterization	Ketchen	451
<i>Transistors</i>		
Optimizing CMOS technology for maximum performance	Frank	419
Ultralow-voltage, minimum-energy CMOS	Hanson	469
<i>Transistors, bipolar</i>		
Bipolar spintronics: Fundamentals and applications	Žutić	121
<i>Web services</i>		
Building web services for scientific grid applications	Kandaswamy	249